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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/810,420	03/19/2001	Yasunori Inoue	57810-020	6503

7590 09/17/2003  
McDERMOTT, WILL & EMERY  
600 13th Street, N.W.  
Washington, DC 20005-3096

EXAMINER

CLARK, SHEILA V

ART UNIT	PAPER NUMBER
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2815

DATE MAILED: 09/17/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

# Office Action Summary

Application No.  
09/810,420

Applicant(s)  
Inoue et al

Examiner  
Sheila V. Clark

Art Unit  
2815



-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

## Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE three MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136 (a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

- 1) ☒ Responsive to communication(s) filed on May 27, 2003 and 8-29-03
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11; 453 O.G. 213.

## Disposition of Claims

- 4) ☒ Claim(s) 1-4, 6, and 9-22 is/are pending in the application.
- 4a) Of the above, claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 9-22 is/are allowed.
- 6) ☒ Claim(s) 1-4 and 6 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claims \_\_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on \_\_\_\_\_ is: a) ☐ approved b) ☐ disapproved by the Examiner.  
If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

## Priority under 35 U.S.C. §§ 119 and 120

- 13) ☒ Acknowledgement is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
a) ☒ All b) ☐ Some\* c) ☐ None of:  
1. ☒ Certified copies of the priority documents have been received.  
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_  
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).  
\*See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgement is made of a claim for domestic priority under 35 U.S.C. § 119(e).  
a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgement is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

## Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892) 4) ☐ Interview Summary (PTO-413) Paper No(s). \_\_\_\_\_
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948) 5) ☐ Notice of Informal Patent Application (PTO-152)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449) Paper No(s). 8 & 12 6) ☐ Other:

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This action has been resent in response to the communication filed 8-29-2003 whereby a PTO 892 was inadvertently not submitted with the last office action. Applicant is to note that the spelling of a prior art reference name in the last rejection of this office action has been corrected to read Haruta et al instead of Hayride

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 1 and 4 are rejected under 35 U.S.C. 103(a) as being unpatentable over Admitted prior art in the disclosure on page 2, lines 19-25.

Page 2, lines 19-25 recite that at least one chip is connected to every input/output terminal of the DRAM chip and the logic chip. This begins the case then a first chip is connected to both a DRAM and a logic chip whereby said second and third chip would obviously be connected to each other through said first chip.

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

Claims 1, 2, 6 are rejected under 35 U.S.C. 102(a) as anticipated by or, in the alternative, under 35 U.S.C. 103(a) as obvious over Ahn et al. .

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Ahn et al shows in 2 a second DRAM chip 110 and a third logic chip 120 formed through first interposer chip 100. Said interposer may be formed of a passive component and obviously comprise at least one of a resistor or capacitor (col. 4, line 38).

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claim 3 is rejected under 35 U.S.C. 103(a) as being unpatentable over Ahn et al.

Interposer are flexible devices and may be formed of one or many components wherein said one component may obviously be a passive component.

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claim 4 is rejected under 35 U.S.C. 103(a) as being unpatentable over Ohashi et al. .

Ohashi et al show a semiconductor chip device and teach in col. 7, lines 60-61 forming power correction circuitry called a PFC circuit of only passive components that include a resistor 112, capacitor 118 and a reactor 114. Col. 9, lines 41-46 teaches that this circuit has an independent function. And col. 12, line 1 teaches that said components can also be integrated in one chip along with a switching circuit to form a filter. This suggests that these components may

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form a chip. The formation of a chip is not limited to one combination of circuitry but any combination of components may be formed into chips and as is well known to one having ordinary skill in this art. Many aspects of circuitry including the passive components of the PRC circuitry only may be formed into a single chip thus the formation of integrated circuit device.

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

Claims 4 is rejected under 35 U.S.C. 102(a) as anticipated by or, in the alternative, under 35 U.S.C. 103(a) as obvious over Kondo and Haruta et al.

Kondo shows a semiconductor chip consisting of a passive component in the form of an oscillator Gunn Diode. Figure 2 show said diode formed of a resistor 44, capacitor, 42 and reactor 40. Haruta et al shows the same device as Kondo in figure 8 and is utilized because it uses the same terminology to describe the device as those recited in the claims ( in col.1, line 29-32, reactor, capacitor and resistor).

Claims 1- 4 and 6 are rejected. Claims 9-22 are considered allowable over the prior art of record.

Takemae et al shows logic chip and DRAM chip connected through an I/O device.

Applicant's arguments with respect to the claim have been considered but are moot in view of the new ground(s) of rejection. The claims continue to be very broad and recite features that

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may be found to be well known. in various different types of devices as shown by the references relied upon the rejection teach. The claims fail to recite features that recite that the recite what the specification describes as novel but extracts pieces of the invention which alone are common to other devices.

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Examiner S.V. Clark whose telephone number is (703) 308-4924. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eddie Lee, can be reached on (703) 308-1690. The fax phone number for the organization where this application or proceeding is assigned is (703) 308-7722 or 7724. Any inquiry of a general nature

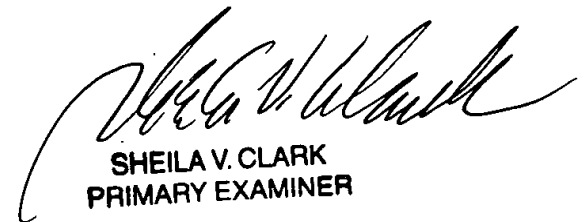
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or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

August 9, 2003



SHEILA V. CLARK  
PRIMARY EXAMINER